Outlay for Calorimeter Pulser Communication

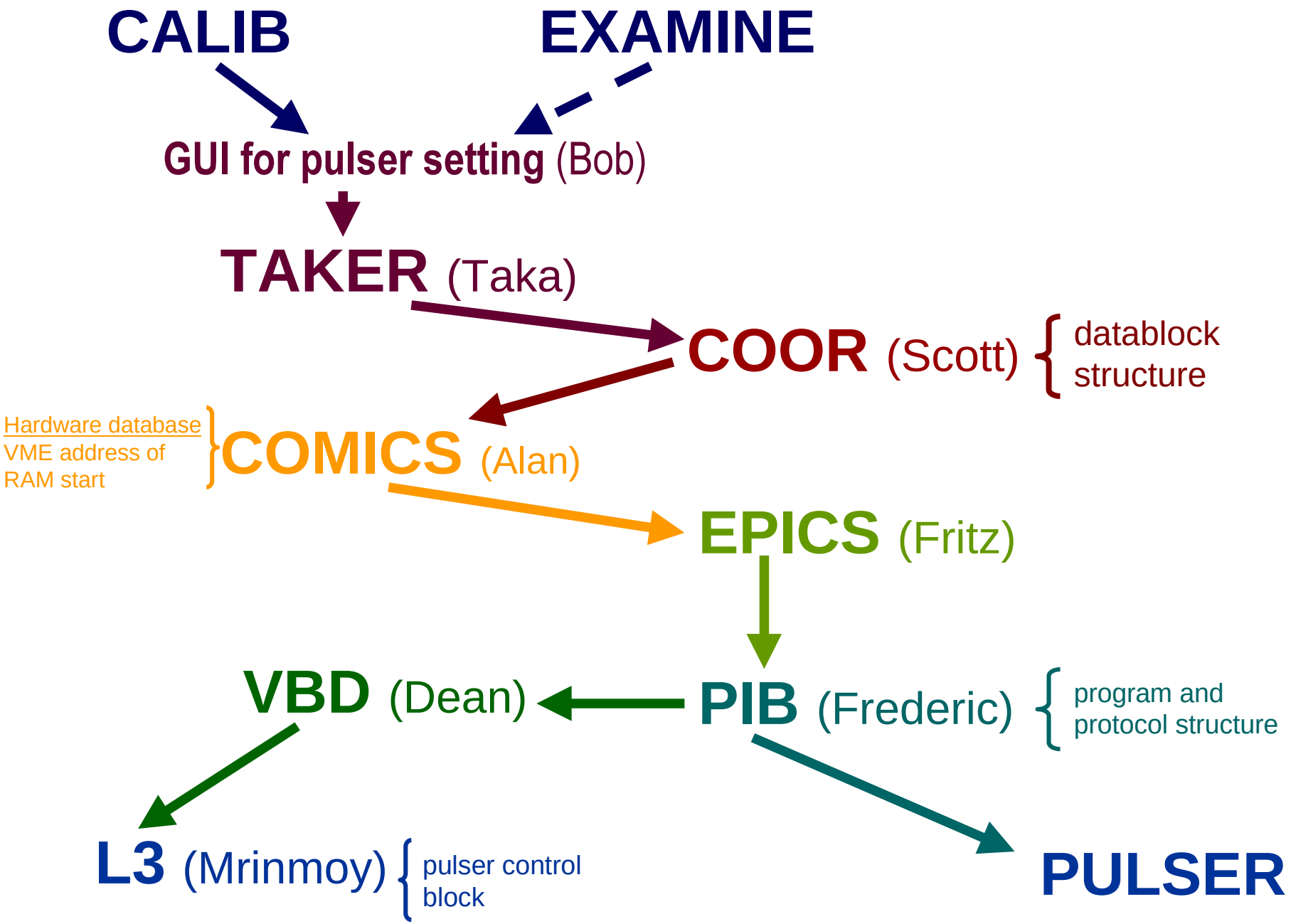
Summary of dedicated

D0 calorimeter electronics meeting

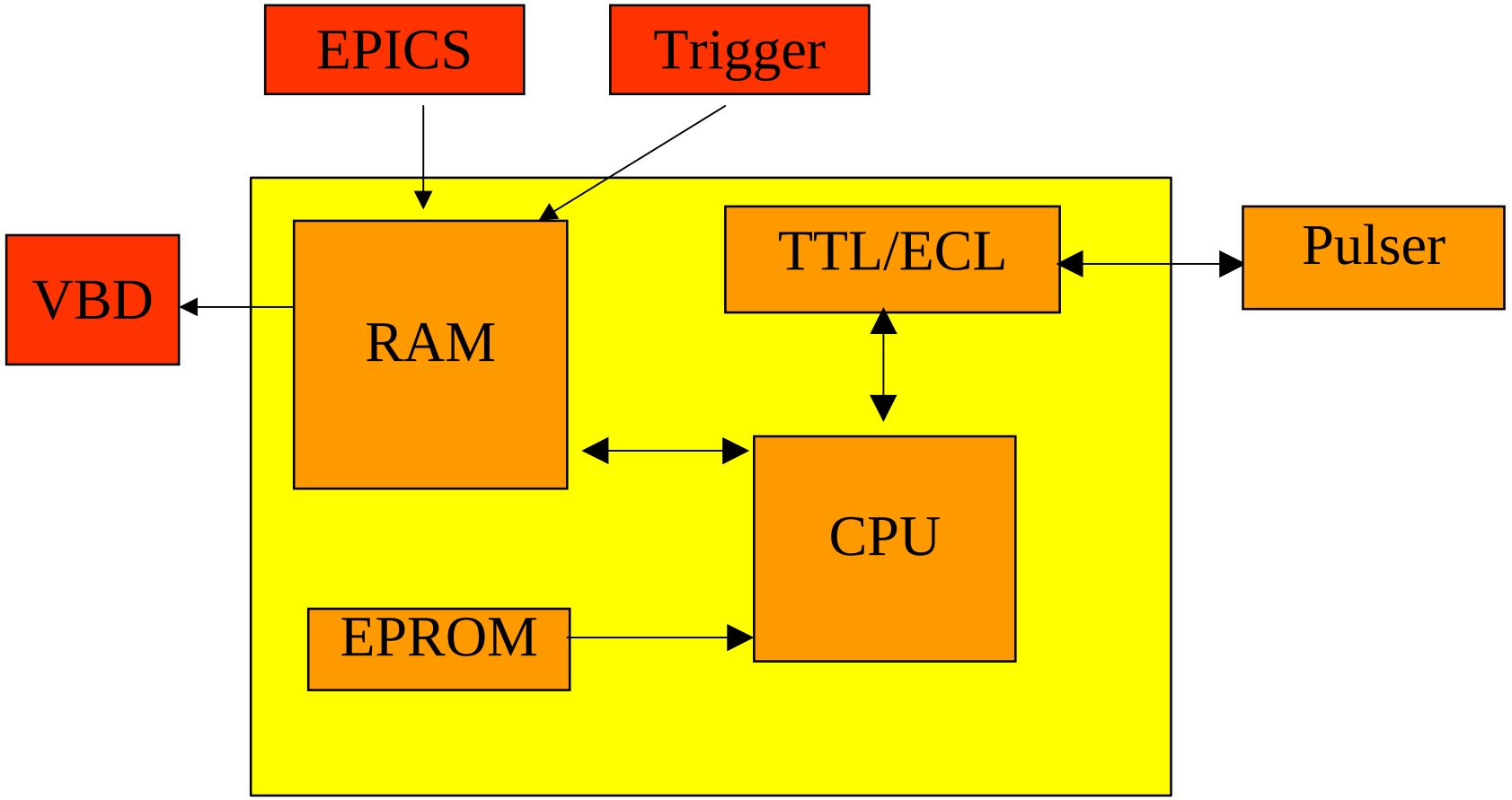
FNAL, 9/10/99

Fritz Barrtelet, Ursula Bassler, Mrinmoy Bhattacharjee, Leslie Groer,
Frederic Machefert, Bob McCarthy, Bob Olivier, Dean Schamberger

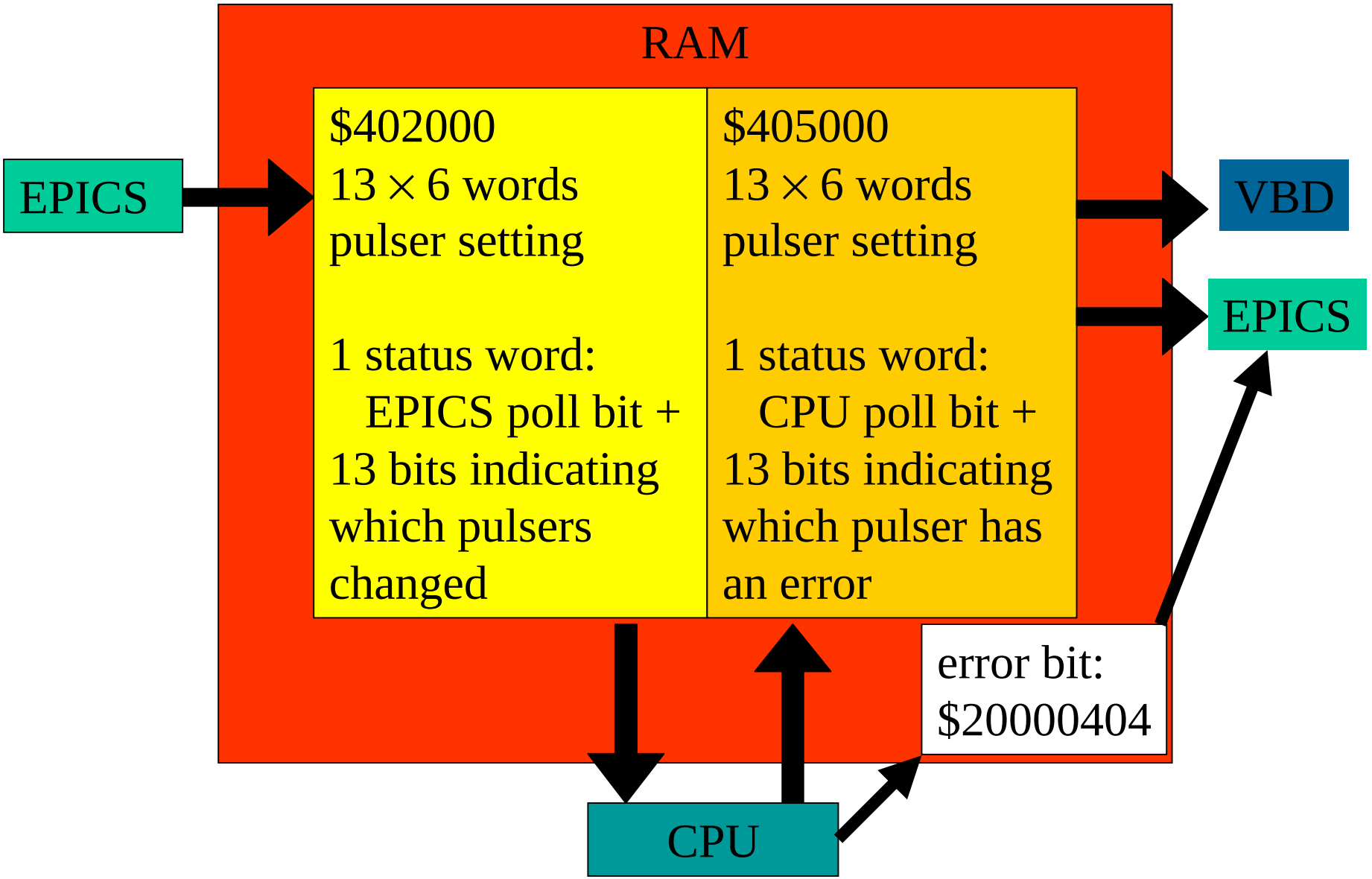
Rampe update: 13/6/00


CALIB
EXAMINE
GUI for pulser setting (Bob)
TAKER (Taka)
COOR (Scott)
datablock structure
Hardware database
VME address of RAM start
COMICS (Alan)
EPICS (Fritz)
VBD (Dean)
PIB (Frederic)
program and protocol structure
L3 (Mrinmoy)
pulser control block
PULSER

PIB - Pulser Interface Board

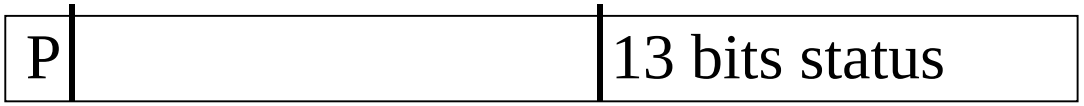


9U VME board constructed qround commercial CPU-board:
CES - FIC 8230/Motorola 6820


RAM
EPICS
$402000
13 × 6 words
pulser setting
1 status word:
EPICS poll bit +
13 bits indicating
which pulsers
changed
$405000
13 × 6 words
pulser setting
1 status word:
CPU poll bit +
13 bits indicating
which pulser has
an error
VBD
EPICS
error bit:
$20000404
CPU

Handshake Protocol EPICS-CPU

status word format:



P: ''Poll'' bit used for handshake:

13 status bits